

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Umemoto, et al.

Serial No.: Not Yet Assigned

Group Art Unit: Unknown

Filed: October 16, 2003

Examiner: Unknown

Title: SEMICONDUCTOR INTEGRATED CIRCUIT MAKING USE OF
STANDARD CELLS

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I hereby certify that this paper is being deposited with the United States Postal Service "EXPRESS MAIL Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and is addressed to: MAIL STOP PATENT APPLICATION, Assistant Commissioner for Patents, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450.



Susan C. Pingue

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PRELIMINARY AMENDMENT TO DIVISIONAL APPLICATION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

ATTN: MAIL STOP PATENT APPLICATION

Sir:

Prior to examination of the above-identified application, which is a divisional of application serial number 09/819,532 filed on March 27, 2001:

Amendments to the Specification are reflected on page 2 of this paper.

Amendments to the Claims are reflected in the listing of claims which begins on page 3 of this paper.